# Electronics Serial Communication-SPI

Terry Sturtevant

Wilfrid Laurier University

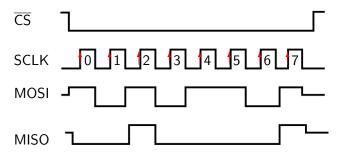
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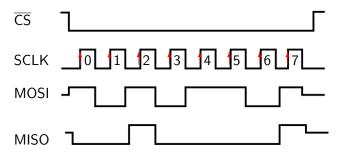
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   and chip selects for each slave device
- Synchronous, so master controls clock rate





SPI transfers can happen in both directions simultaneously.

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   It is designated as port 0
   SPI0 has two associated devices, selected by chip selects
   CE0 and CE1
- So, spi.open(0,0) means to connect to the device using CE0 on SPI0

(continued)

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- response = spi.xfer2([0xAA])
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   CS held active between blocks
- response = spi.xfer([values])
   transfer bytes
   CS realeased and reactivated between blocks

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  read len bytes
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   get or set active state of CS
- spi.close()
  close port

#### PySpidev sample code

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```
import spidev
spi = spidev.SpiDev()
spi.open(0,0) \#port, device
# use port 0, chip select CE0
spi.max\_speed\_hz = 5000
while True:
  strval = input("val(0...255, q=quit):")
  if strval == 'q':
    break
  else ·
    value = int (strval)
    dummy = spi.xfer2([49,value])
spi.close()
```