

PC/CP220 Project Phase III Checklist (3.0)

A. General

1. Professionally presented  
Firmly bound (eg. stapled), neat, etc. \_\_\_\_\_
2. Properly identified  
(eg. name, partner, id) \_\_\_\_\_
3. On time  
at *beginning* of lab *with checklist* \_\_\_\_\_
4. Good grammar  
(eg. complete sentences where required) \_\_\_\_\_
5. Correct spelling \_\_\_\_\_

B. Content

1. *Circuit diagram is  
consistent with equations* \_\_\_\_\_
2. *All cases are simulated  
with good explanations* \_\_\_\_\_
3. *Circuit passes/fails simulation  
noted (matches truth table)* \_\_\_\_\_
4. *All input/output parts have been  
listed (including resistors)* \_\_\_\_\_
5. *All previous phases included  
( updated if necessary)* \_\_\_\_\_