

PC/CP220 Project Phase III Checklist (3.0)

A. General

1. Professionally presented
Firmly bound (eg. stapled), neat, etc. _____
2. Properly identified
(eg. name, partner, id) _____
3. On time
at *beginning* of lab *with checklist* _____
4. Good grammar
(eg. complete sentences where required) _____
5. Correct spelling _____

B. Content

1. *Circuit diagram is
consistent with equations* _____
2. *All cases are simulated
with good explanations* _____
3. *Circuit passes/fails simulation
noted (matches truth table)* _____
4. *All input/output parts have been
listed (including resistors)* _____
5. *All previous phases included
(updated if necessary)* _____