

PC/CP220 Project Phase I Checklist (3.0)

A. General

1. Professionally presented
Firmly bound (eg. stapled), neat, etc. _____
2. Properly identified
(eg. name, partner, id) _____
3. On time
at *beginning* of lab *with checklist* _____
4. Good grammar
(eg. complete sentences where required) _____
5. Correct spelling _____

B. Content

1. *Sufficient background*
(ie. *problem circuit is to solve*) _____
2. *Inputs are specified*
(eg. A_0, A_1, A_2, A_3 - *binary*) _____
3. *Outputs are specified*
(eg. L_1, S_1, L_2, S_2 , etc. - *dashes/dots*) _____
4. *Error conditions and responses*
to them are specified
(or *stated that none exist*) _____
5. *Ambiguous possibilities have been*
eliminated (ie. don't care conditions)
(or *stated that none exist*) _____