## PC/CP120 Project Phase III Checklist (3.0)

| A.         | Genera | ıl   |  |
|------------|--------|--|--|
|            | 1.     | Professionally presented<br>Firmly bound (eg. stapled), neat, etc. |  |
|            | 2.     | Properly identified (eg. name, partner, id)                        |  |
|            | 3.     | On time at beginning of lab with checklist                         |  |
|            | 4.     | Good grammar (eg. complete sentences where required)               |  |
|            | 5.     | Correct spelling   |  |
| B. Content |        |  |  |
|            | 1.     | Circuit diagram is consistent with equations                       |  |
|            | 2.     | All cases are simulated with good explanations                     |  |
|            | 3.     | Circuit passes/fails simulation noted (matches truth table)        |  |
|            | 4.     | All input/output parts have been listed (including resistors)      |  |
|            | 5.     | All previous phases included (updated if necessary)                |  |