

PC/CP120 Project Phase I Checklist (3.0)

A. General

1. Professionally presented  
Firmly bound (eg. stapled), neat, etc. \_\_\_\_\_
2. Properly identified  
(eg. name, partner, id) \_\_\_\_\_
3. On time  
at *beginning* of lab *with checklist* \_\_\_\_\_
4. Good grammar  
(eg. complete sentences where required) \_\_\_\_\_
5. Correct spelling \_\_\_\_\_

B. Content

1. *Sufficient background*  
(*ie. problem circuit is to solve*) \_\_\_\_\_
2. *Inputs are specified*  
(*eg.  $A_0, A_1, A_2, A_3$  - binary*) \_\_\_\_\_
3. *Outputs are specified*  
(*eg.  $L_1, S_1, L_2, S_2$ , etc. - dashes/dots*) \_\_\_\_\_
4. *Error conditions and responses*  
*to them are specified*  
(*or stated that none exist*) \_\_\_\_\_
5. *Ambiguous possibilities have been*  
*eliminated (ie. don't care conditions)*  
(*or stated that none exist*) \_\_\_\_\_