PC/CP120 Digital Design Lab Project Marking Sheet (4.0)

Project name:	
Student names:	
Lab section:	
Circuit-Visual Circuit Layout (placement, colour, wiring)	Poor=1, Excellent=5
Good choice of input device	
Good choice of output device	
Circuit-Testing	Poor=1, Excellent=5
Inputs clearly labeled	
Outputs clearly labeled	
Clear operating instructions	
$Test\ successful$	
Poster	Poor=1, Excellent=5
Professional appearance	
Clear, correct language	
Design process illustrated (K-maps, Circuit Diagram, Circuit Simulation, etc.)	