

CP316

Analog to Digital Converters

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Analog to Digital Converters

Analog to Digital Converters

overview

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→ Section 12.3

registers

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→ Section 12.3.1

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integrated sample and hold

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10 bit operation

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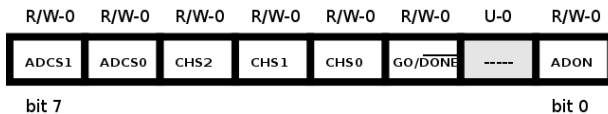
→ Section 17.0

10 bit operation

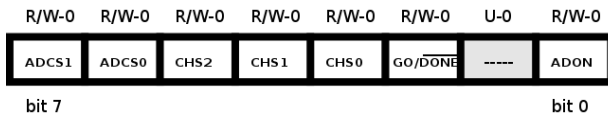
→ Section 17.4.1

ADCON0

ADCON0

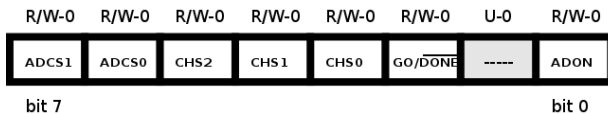


ADCON0



Bits in ADCON0 register

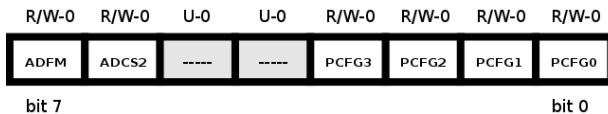
ADCON0



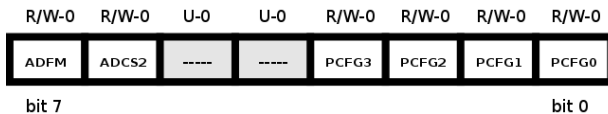
Bits in ADCON0 register - Note ADCS1, ADCS0

ADCON1

ADCON1

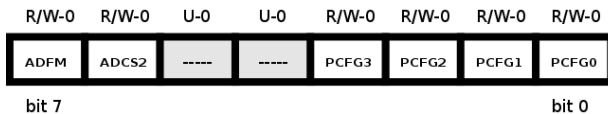


ADCON1



Bits in ADCON1 register

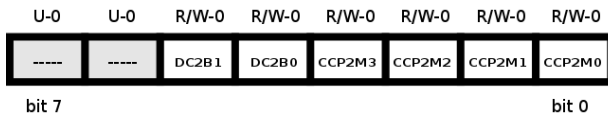
ADCON1



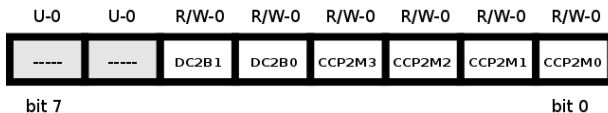
Bits in ADCON1 register - Note ADCS2

CCP2CON

CCP2CON

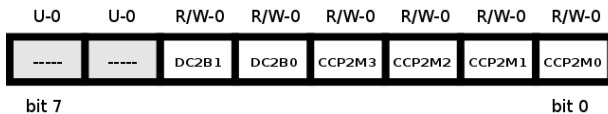


CCP2CON



Bits in CCP2CON register

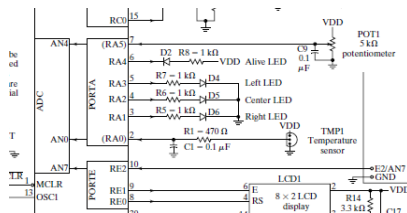
CCP2CON



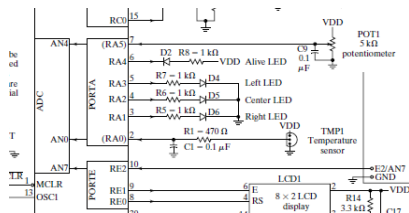
Bits in CCP2CON register -Mode 1011 can start ADC

Qwikflash ADC connections

Qwikflash ADC connections



Qwikflash ADC connections



ramifications???

Initialllization

Initiallization

overview

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overview

→ Section 12.4, step 1

Initiallization

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→ Section 12.4, step 1

interrupts

Initialization

overview

→ Section 12.4, step 1

interrupts

→ Section 6.4.5

Initialization

overview

→ Section 12.4, step 1

interrupts

→ Section 6.4.5

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Code

Code

overview

Code

overview

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Code

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→ Section 12.4

PORT configuration

Code

overview

→ Section 12.4

PORT configuration

→ macro or subroutine?

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Initialization

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Initialization

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Start conversion

Code

overview

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→ macro or subroutine?

Initialization

→ macro or subroutine?

Start conversion

→ macro or subroutine?

Read value

Code

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