Definitions for Week 5

- interrupts; high vs. low priority

- interrupt sources; external and internal

- external interrupts; edge- or level-triggered

- interrupt bits; enabled, priority, flag

- interrupt status; clear, pending

- interrupt service routines; vectors, flags (including clearing), RETFIE

- interrupt initiallization; setup, clear flags, enable

- interrupts and shadow registers; RETFIE FAST