Definitions for Week 1

- microcontroller vs. microprocessor vs. digital signal processor

- Harvard vs. Von Neuman architecture

- file register

- special function register

- general purpose register

- access bank

- bsr

- W register

- WREG

- assembly language operands (,F ,W ,A ,BANKED)

- 16 banks of SFRs

- FSR vs. BSR

- RISC vs. CISC - e.g MOV instruction - complexity of assembler; execution time

- absolute vs. relocatable code

- program "end" is an infinite loop!