

CP316

Program and Data EEPROM Memory

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memory not usually written to in simple programs

Program and Data EEPROM Memory

memory not usually written to in simple programs
→ Chapter 14

Program and Data EEPROM Memory

memory not usually written to in simple programs

→ Chapter 14

→ Chapters 5 and 6

Program Memory

Program Memory

→ Chapter 5

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→ Chapter 5
uses TABLWT

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8 bytes at a time

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8 bytes at a time
erased 64 bytes at a time

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Program Memory

→ Chapter 5

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(in 8 byte chunks)

Program Memory

→ Chapter 5

uses TABLWT

8 bytes at a time

erased 64 bytes at a time

must be erased before writing

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(in 8 byte chunks)

watch block alignment

Program Memory

→ Chapter 5

uses TABLWT

8 bytes at a time

erased 64 bytes at a time

must be erased before writing

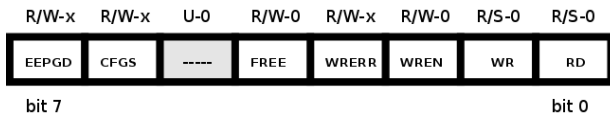
thus need to read 64, erase all, re-write 64 with 8 changed
(in 8 byte chunks)

watch block alignment

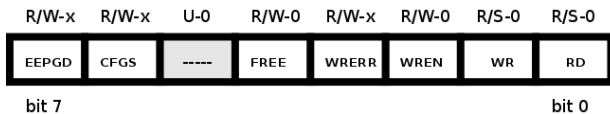
instruction fetching halts during write or erase

EECON1

EECON1



EECON1



Bits in EECON1 register

Program Memory (continued)

Program Memory (continued)

logical reason?

Program Memory (continued)

logical reason?

rewrite fixed-length string(s)

Program Memory (continued)

logical reason?

rewrite fixed-length string(s)

crazy reason?

Program Memory (continued)

logical reason?

rewrite fixed-length string(s)

crazy reason?

overwrite code !!!

Data EEPROM

Data EEPROM

→ Chapter 6

Data EEPROM

→ Chapter 6

256 bytes

Data EEPROM

→ Chapter 6

256 bytes

not mapped in register file space

Data EEPROM

→ Chapter 6

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not mapped in register file space

indirectly addressable through SFRs

Data EEPROM

→ Chapter 6

256 bytes

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EEADR, (EEADRH), EEDATA

Data EEPROM

→ Chapter 6

256 bytes

not mapped in register file space

indirectly addressable through SFRs

EEADR, (EEADRH), EEDATA

single byte written or read at a time

Data EEPROM

→ Chapter 6

256 bytes

not mapped in register file space

indirectly addressable through SFRs

EEADR, (EEADRH), EEDATA

single byte written or read at a time

read - takes single instruction cycle

Data EEPROM

→ Chapter 6

256 bytes

not mapped in register file space

indirectly addressable through SFRs

EEADR, (EEADRH), EEDATA

single byte written or read at a time

read - takes single instruction cycle

write - EEIF flag (PIR2) set on completion