

Definitions for Week 7

- ADC module - single 10 bit ADC

- AN0-7
 - AN0 RA0
 - AN1 RA1
 - AN2 RA2
 - AN3 RA3
 - AN4 RA5
 - AN5 RE0
 - AN6 RE1
 - AN7 RE2

- ADRESL, ADRESH - left or right justified

- UART

- -RS232 levels ($-3 \rightarrow -15$; $+3 \rightarrow +15$) INVERTED

- - “mark” - idle level, negative, **1**

- - “space” - positive, **0**

- - “start bit” - space level, positive, **0**

- - “stop bit” - mark level, negative, **1**
- -RS232 bit order LSB first
- **TTL serial possibilities**
- inverted bits; assume 0 acceptable “mark” level