

Definitions for Week 2

- label, mnemonic, operand(s), comment
- instructions vs. directives
- branch instructions and range; compare with **goto**
- \$ as “address of current instruction”
- skip instructions
- macros vs. subroutines
- CALL (,FAST), RCALL, RETURN (FAST), RETLW
- hardware stack vs. software stack
- return address stack
- STKPNTR register
- shadow registers

- fast register stack
- I/O ports and multifunction pins
- modular I/O bit configuration
- **TRIS, PORT, LAT** registers